HXMH65M45P

N-Channel Power MOSFET

650 V, 62.6 A, 45 mΩ

Description

The 650V FRD series has ultra-fast body diode performance，low on-resistance and reduced gate charge.

It combines the benefits of a fast switching performance with ease of usage and robustness. Additionally, we offer low reverse recovery time (trr) and reverse recovery charge (Qrr) for the bridge structure topology, especially for resonant converters (LLC, PSFB, etc.).

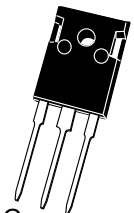
Features

BV_{DSS} @ $T_{J,max}$	I_D	$R_{DS(on),max}$	$Q_{g,typ}$
700 V	62.6 A	45 mΩ	142 nC

- Reduced Switching & Conduction Losses
- Fast Recovery Body-Diode
- Lower Gate Resistance
- 100% Avalanche Tested
- Pb-free and RoHS Compliant
- Compliance with EU REACH

Applications

- Soft Switching Topologies
- Telecom and Sever Power Supplies
- EV Charger and Industrial Power Supplies



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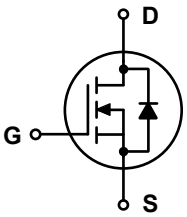


Absolute Maximum Ratings (T_C = 25℃ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		650	V
V_{GSS}	Gate to Source Voltage		±30	V
I_D	Drain Current	Continuous (T_C = 25℃)	62.6	A
		Continuous (T_C = 100℃)	39.6	
I_{DM}	Drain Current	Pulsed (Note1)	188	A
E_{AS}	Single Pulsed Avalanche Energy	(Note2)	457	mJ
I_{AS}	Avalanche Current	(Note2)	8.4	A
E_{AR}	Repetitive Avalanche Energy	(Note1)	4.46	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt	(Note3)	50	
P_D	Power Dissipation	(T_C = 25℃)	446	W
		Derate Above 25℃	3.57	W/℃
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to 150	℃
T_L	Maximum Lead Temperature for Soldering, 1/8” from Case for 10 Seconds		260	℃

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.28	℃/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
HXMH65M45P	H65M45P	TO-247AB	Tube	30 units

Electrical Characteristics (T_C = 25℃ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 1 mA	650			V
		V_{GS} = 0 V, I_D = 1 mA, T_J = 150℃	700			
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 650 V, V_{GS} = 0 V			10	μA
		V_{DS} = 520 V, V_{GS} = 0 V, T_J = 125℃		57		
I_{GSS}	Gate-Source Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V			±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 4.3 mA	3.0		5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 31.3 A		38.5	45	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0V, f = 250 kHz		5451		pF
C_{oss}	Output Capacitance			135		pF
$C_{o(tr)}$	Time Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		1477		pF
$C_{o(er)}$	Energy Related Output Capacitance			199		pF
$Q_{g(tot)}$	Total Gate Charge at 10 V	V_{DS} = 400 V, I_D = 31.3 A, V_{GS} = 10 V		142		nC
Q_{gs}	Gate to Source Charge			36		nC
Q_{gd}	Gate to Drain “Miller” Charge			74		nC
R_G	Gate Resistance	f = 1MHz		1		Ω
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DS} = 400 V, I_D = 31.3 A, V_{GS} = 10 V, R_G = 3.3 Ω See Figure 13		27		ns
t_r	Turn-On Rise Time			15		ns
$t_{d(off)}$	Turn-Off Delay Time			87		ns
t_f	Turn-Off Fall Time			11		ns
Source-Drain Diode Characteristics						
I_S	Maximum Continuous Diode Forward Current				62.6	A
I_{SM}	Maximum Pulsed Diode Forward Current				188	A
V_{SD}	Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 31.3 A			1.2	V
t_{rr}	Reverse Recovery Time	V_{DD} = 400 V, I_{SD} = 31.3 A, dI_F/dt = 100 A/μs		170		ns
Q_{rr}	Reverse Recovery Charge			1.3		μC

※Notes:
1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. I_{AS} = 8.4 A, R_G = 25 Ω, starting T_J = 25℃.
3. I_{SD} ≤ 31.3 A, di/dt ≤ 100 A/μs, V_{DD} ≤ 400 V, starting T_J = 25℃.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

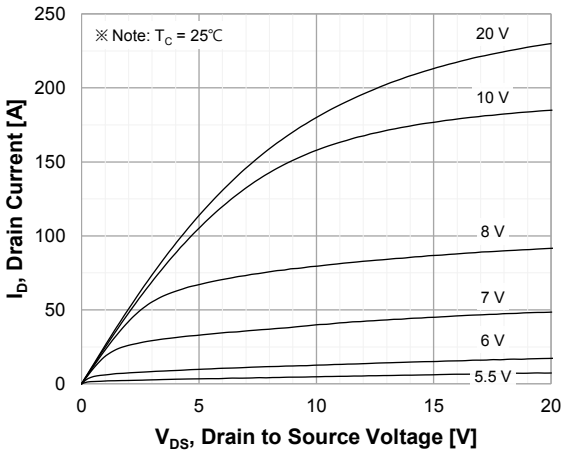


Figure 2. Transfer Characteristics

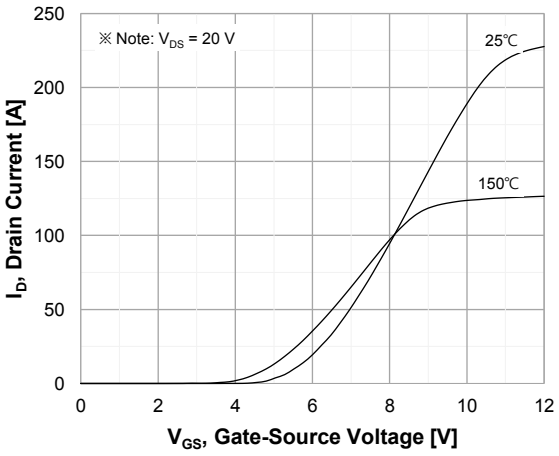


Figure 3. On-Resistance Characteristics vs. Drain Current and Gate Voltage

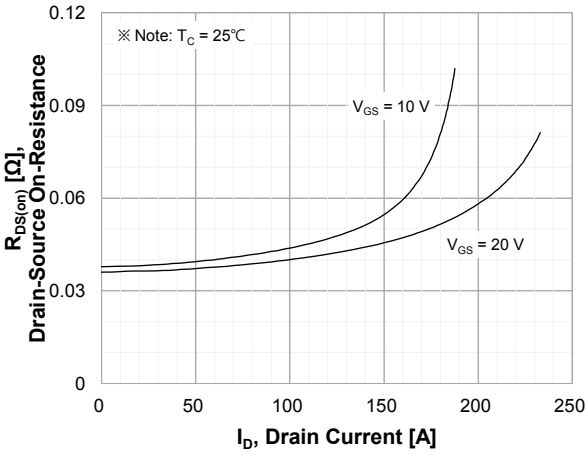


Figure 4. Diode Forward Voltage Characteristics vs. Source-Drain Current and Temperature

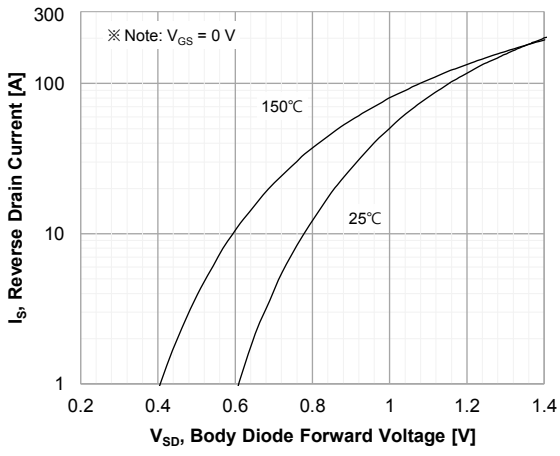


Figure 5. Capacitance Characteristics

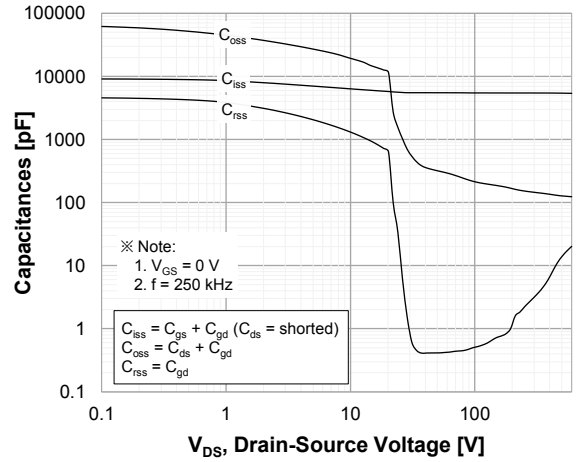
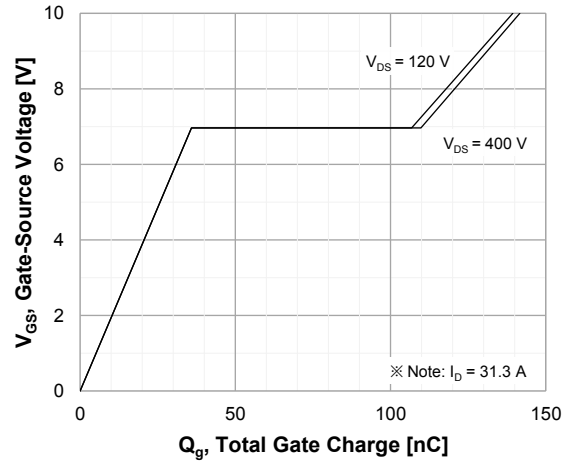


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Characteristics vs. Temperature

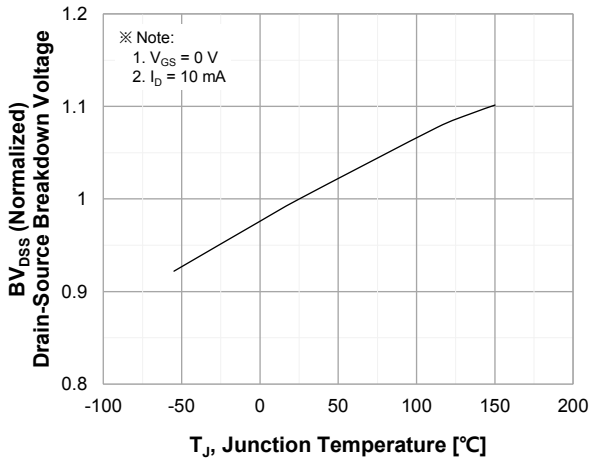


Figure 8. On-Resistance Characteristics vs. Temperature

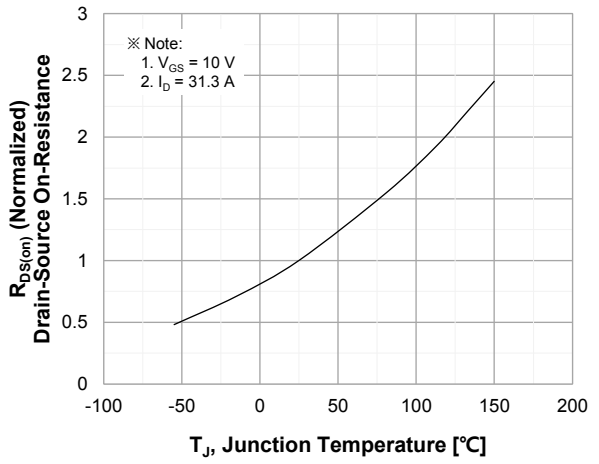


Figure 9. Maximum Safe Operating Area

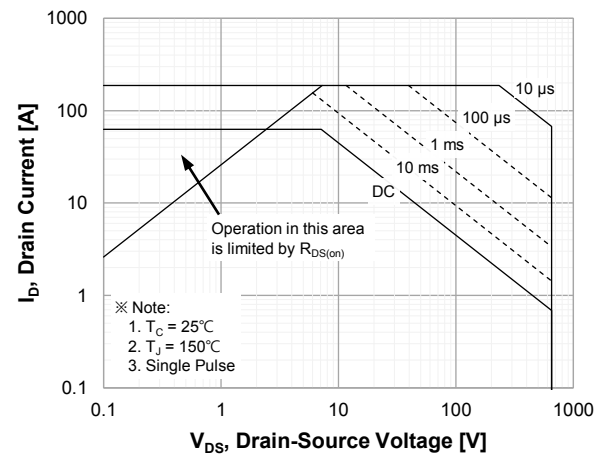


Figure 10. Maximum Drain Current vs. Case Temperature

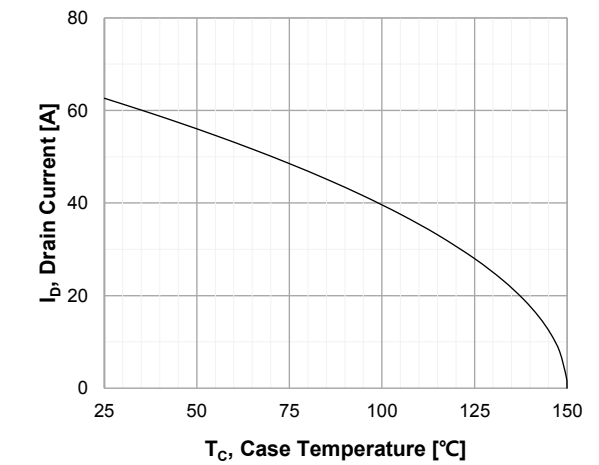


Figure 11. E_{oss} vs. Drain to Source Voltage

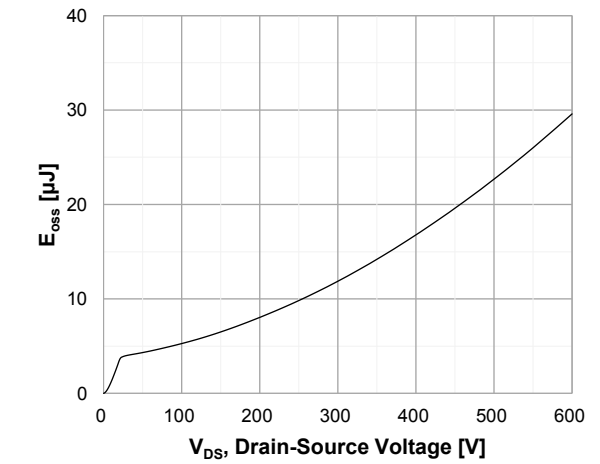
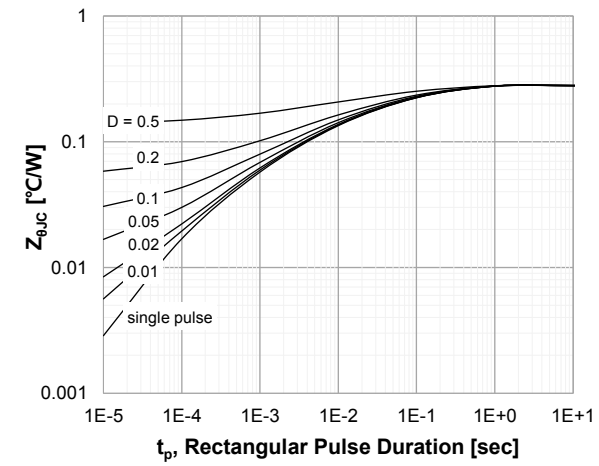


Figure 12. Transient Thermal Response Curve



Test Circuits

Figure 13. Inductive Load Switching Test Circuit and Waveforms

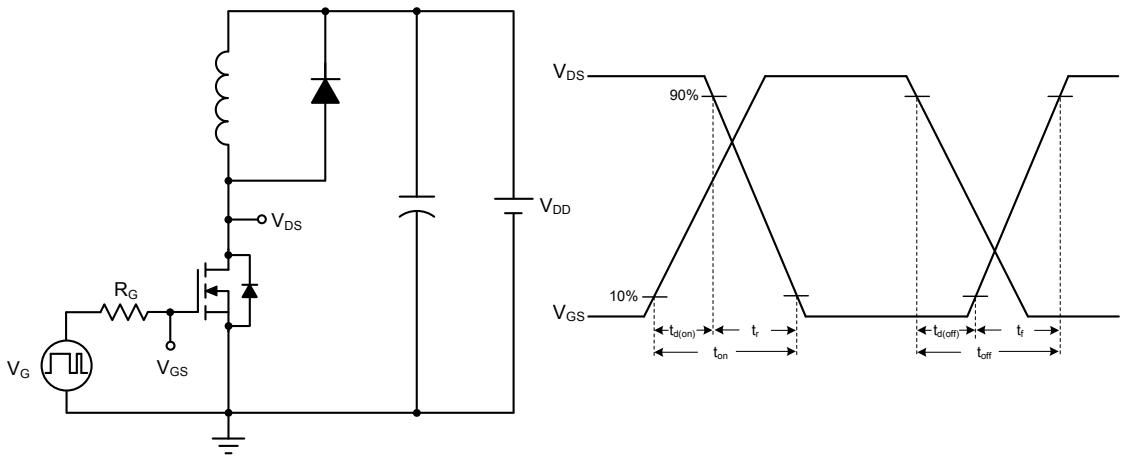


Figure 14. Unclamped Inductive Switching Test Circuit and Waveforms

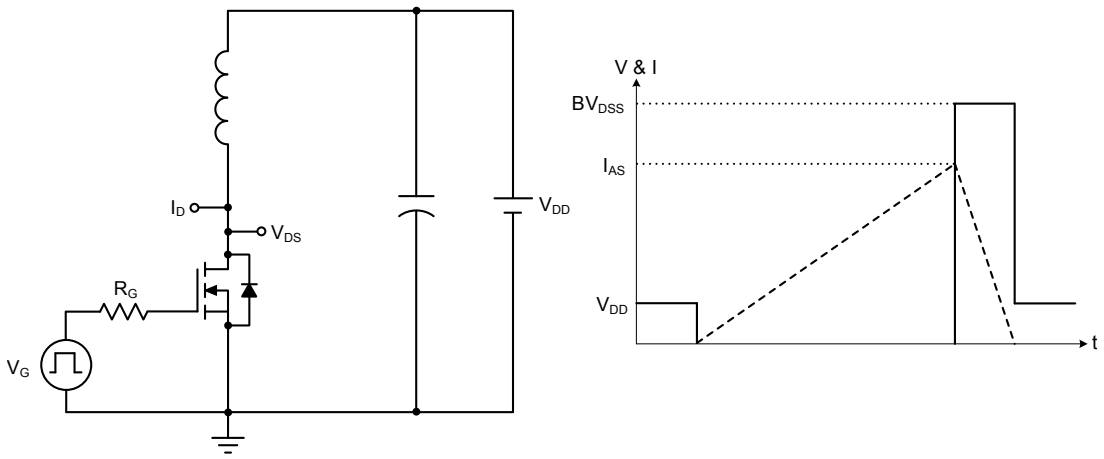
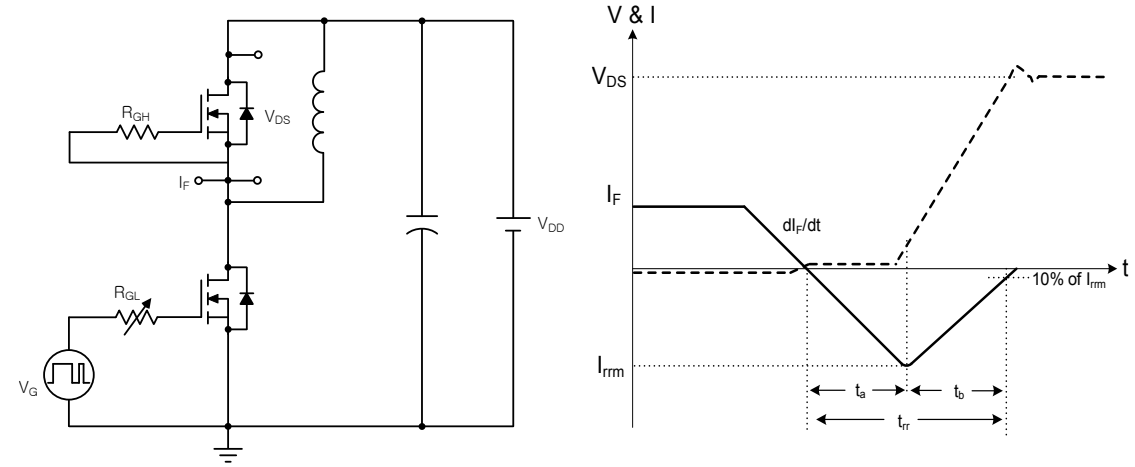


Figure 15. Peak Diode Recovery dv/dt Test Circuit and Waveforms



Package Outlines

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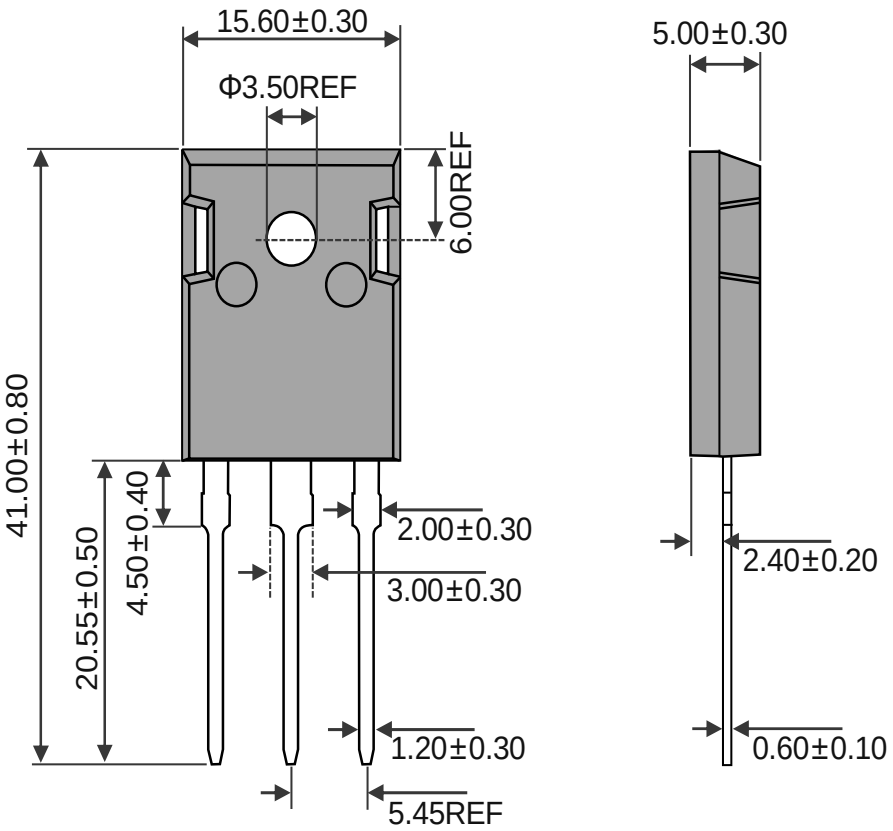

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Unit:mm

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