# **HXMC65N160S3**N-Channel SiC Power MOSFET MOSFET

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#### **Features**

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitance
- Easy to Parallel and Simple to Drive
- 15V / 0V V<sub>GS</sub> compatible with most flyback controllers

V <sub>DS</sub>	=	650	V
R <sub>DS(on)</sub>	=	160	$m\Omega$
I <sub>D</sub> @25°C	=	20	A

#### **Benefits**

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency
- Reduction of heat sink requirements





#### TO-252-2L

### **Applications**

- Solar and UPS inverters
- On Board Charger
- High voltage DC/DC converters
- Switched mode power supplies
- Load switch
- LED/LCD/PDP TV and monitor Lighting



### Maximum Ratings (T<sub>c</sub>=25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note	
<b>V</b> <sub>DSmax</sub>	Drain-Source Voltage	650	٧	V <sub>GS</sub> =0V, I <sub>D</sub> =100μA		
V <sub>GSmax</sub>	Gate-Source Voltage	-10/+23	٧	Absolute maximum values		
$V_{GSop}$	Gate-Source Voltage	-50/+15+18	٧	Recommended operational values		
	Continuous Drain Current	20		V <sub>GS</sub> =15V, T <sub>c</sub> =25°C	Fig. 19	
I <sub>D</sub>		14	_	V <sub>GS</sub> =15V, T <sub>c</sub> =100°C		
		21	Α	V <sub>GS</sub> =18V, T <sub>c</sub> =25°C		
		15		V <sub>GS</sub> =18V, T <sub>c</sub> =100°C		
I <sub>D(pulse)</sub>	Pulsed Drain Current	50	Α	Pulse width t <sub>p</sub> limited by T <sub>Jmax</sub>		
P <sub>D</sub>	Power Dissipation	95	W	T <sub>c</sub> =25°C, T <sub>J</sub> =175°C	Fig. 20	
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature	-55 to +175	Ĉ			
TL	Solder Temperature, 1.6mm from case for 10s	260	Ĵ			



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## HXMC65N160S3

## **N-Channel SiC Power MOSFET MOSFET**

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#### Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	Note	
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	650	/	/	V	V <sub>GS</sub> =0V, I <sub>D</sub> =100μA		
V Cata T	Cata Threathald Voltage	2.4	3.2	4.0	v	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =2mA	Fig. 11	
$V_{GS(th)}$	Gate Threshold Voltage	/	2.4	/	\ \ \	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =2mA, T <sub>J</sub> =175°C	Fig. 11	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	/	1	10	μΑ	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V		
I <sub>GSS+</sub>	Gate-Source Leakage Current	/	10	50	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =23V		
I <sub>GSS-</sub>	Gate-Source Leakage Current	/	10	50	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =-10V		
		/	160	220	mΩ	V <sub>GS</sub> =15V, I <sub>D</sub> =7A		
ь.	Busin Sayusa On State Besisten as	/	210	/	mΩ	V <sub>GS</sub> =15V, I <sub>D</sub> =7A, T <sub>J</sub> =175 ℃	Fig. 4,5,6	
$R_{DS(on)}$	Drain-Source On-State Resistance	/	130	180	mΩ	V <sub>GS</sub> =18V, I <sub>D</sub> =7A		
		/	173	/	mΩ	V <sub>GS</sub> =18V, I <sub>D</sub> =7A, T <sub>J</sub> =175℃		
		/	6.2	/		V <sub>DS</sub> =20V, I <sub>DS</sub> =7A		
<b>g</b> fs	Transconductance / 5.6 / S	8	V <sub>DS</sub> =20V, I <sub>DS</sub> =7A, T <sub>J</sub> =175 ℃	Fig. 7				
C <sub>iss</sub>	Input Capacitance	/	448	/		V <sub>GS</sub> =0V	Fig.	
C <sub>oss</sub>	Output Capacitance	/	44	/	рF	V <sub>DS</sub> =400V		
C <sub>rss</sub>	Reverse Transfer Capacitance	/	2.2	/		f=1MHz	17,18	
E <sub>oss</sub>	C <sub>oss</sub> Stored Energy	/	2.8	/	μ	V <sub>AC</sub> =25mV	Fig. 16	
E <sub>ON</sub>	Turn-On Switching Energy	/	24	/		V <sub>DS</sub> =400V, V <sub>GS</sub> =0V/15V		
E <sub>OFF</sub>	Turn-Off Switching Energy	/	16	/	μ	I <sub>D</sub> =7A, R <sub>G(ext)</sub> =2.5Ω, L=100μH		
t <sub>d(on)</sub>	Turn-On Delay Time	/	11	/				
t <sub>r</sub>	Rise Time	/	8.6	/		V <sub>DS</sub> =400V, V <sub>GS</sub> =0V/15V, I <sub>D</sub> =7A		
t <sub>d(off)</sub>	Turn-Off Delay Time	/	18.2	/	ns	$R_{G(ext)}=2.5\Omega$ , $R_L=80\Omega$		
t <sub>f</sub>	Fall Time	/	14.6	/				
R <sub>G(int)</sub>	Internal Gate Resistance	/	7	/	Ω	f=1MHz, V <sub>AC</sub> =25mV		
Q <sub>GS</sub>	Gate to Source Charge	/	4.6	/		V <sub>DS</sub> =400V		
Q <sub>GD</sub>	Gate to Drain Charge	/	3.8	/	nC	V <sub>GS</sub> =0V/15V	Fig. 12	
Q <sub>G</sub>	Total Gate Charge	/	12.6	/		I <sub>D</sub> =7A		

#### **Reverse Diode Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V	Diado Famueud Voltage	4.2	/	V	$V_{GS}$ =-5V, $I_{SD}$ =3.5A, $T_{J}$ =25 $^{\circ}$ C	Fig.
V <sub>SD</sub>	Diode Forward Voltage	3.6	/		V <sub>GS</sub> =-5V, I <sub>SD</sub> =3.5A, T <sub>J</sub> =175℃	8,9,10
Is	Continuous Diode Forward Current	/	16	Α	T <sub>C</sub> =25°C	
t <sub>rr</sub>	Reverse Recover Time	12	/	ns		
Q <sub>rr</sub>	Reverse Recovery Charge	28	/	nC	V <sub>R</sub> =400V, I <sub>SD</sub> =3.5A	
I <sub>rrm</sub>	Peak Reverse Recovery Current	1.8	/	Α		



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## **HXMC65N160S3**

## **N-Channel SiC Power MOSFET MOSFET**

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
R <sub>θJC</sub>	Thermal Resistance from Junction to Case	1.58	/	°C /\w/		
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	/	40 °C/W			



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#### **Typical Performance**

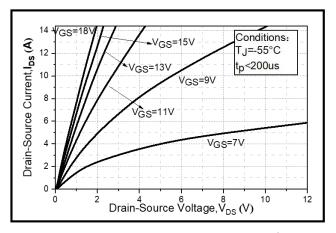


Figure 1. Output Characteristics T<sub>J</sub> = -55°C

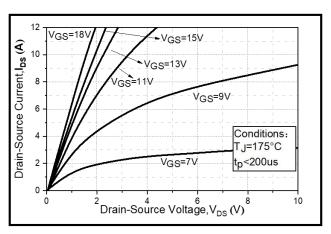


Figure 3. Output Characteristics T<sub>J</sub> = 175°C

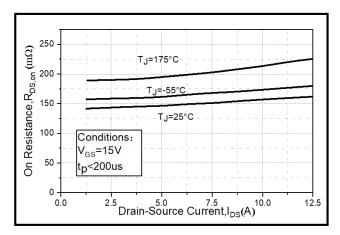


Figure 5. On-Resistance vs. Drain Current For Various
Temperatures

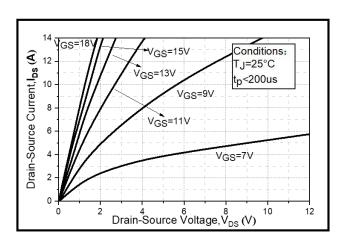


Figure 2. Output Characteristics T<sub>J</sub> = 25°C

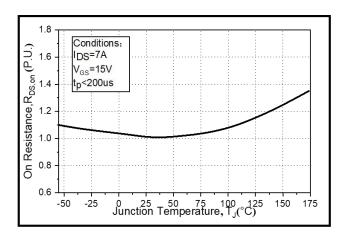


Figure 4. Normalized On-Resistance vs. Temperature

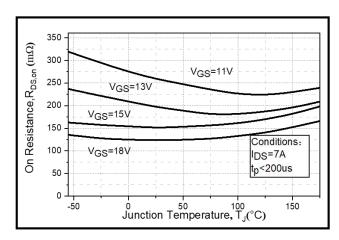


Figure 6. On-Resistance vs. Temperature For Various Gate
Voltage



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#### **Typical Performance**

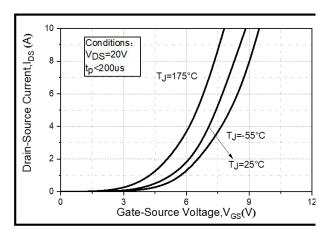


Figure 7. Transfer Characteristic for Various Junction Temperatures

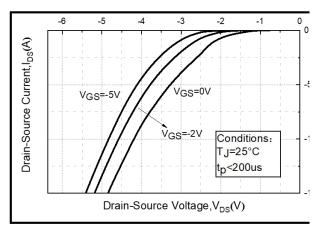


Figure 9. Body Diode Characteristic at 25°C

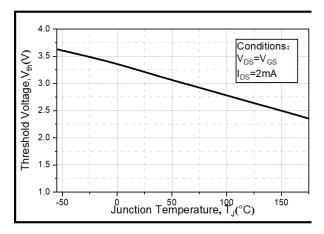


Figure 11. Threshold Voltage vs. Temperature

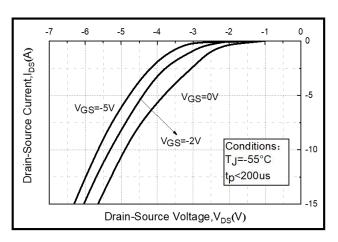


Figure 8. Body Diode Characteristic at -55°C

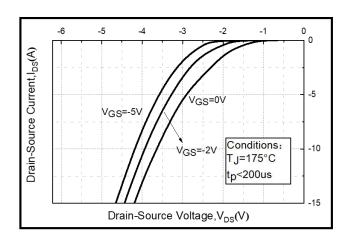


Figure 10. Body Diode Characteristic at 175°C

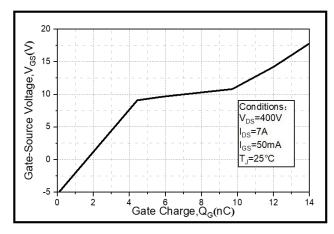


Figure 12. Gate Charge Characteristics

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### **Typical Performance**

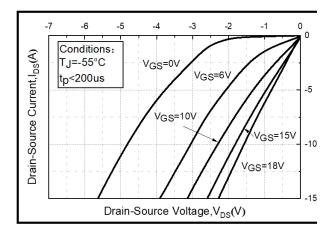


Figure 13. 3rd Quadrant Characteristic at-55°C

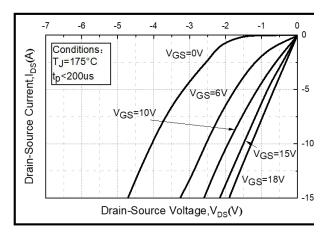


Figure 15. 3rd Quadrant Characteristic at 175°C

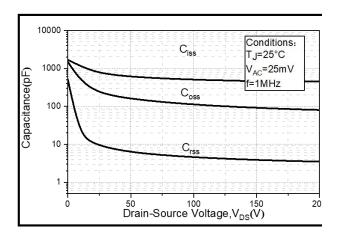


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

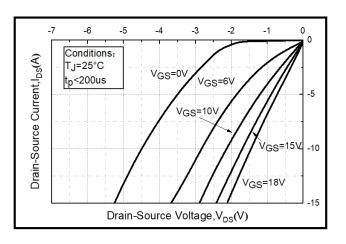


Figure 14. 3rd Quadrant Characteristic at 25°C

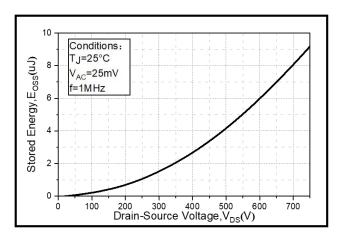


Figure 16. Output Capacitor Stored Energy

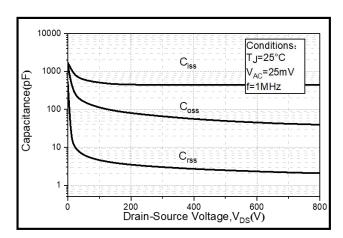


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 800V)



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## **Typical Performance**

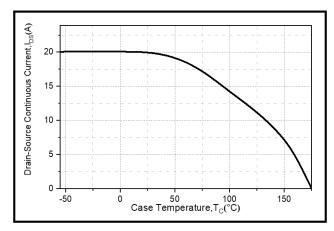


Figure 19. Continuous Drain Current vs.

Case Temperature

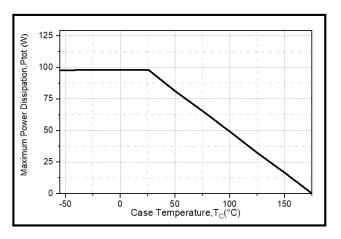


Figure 20. Maximum Power Dissipation vs.

Case Temperature



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#### **Test Circuit Schematic**

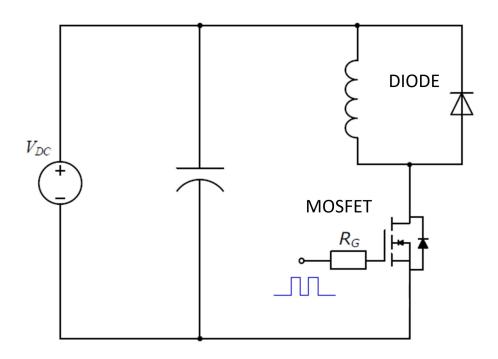


Figure 21. Clamped Inductive Switching
Waveform Test Circuit

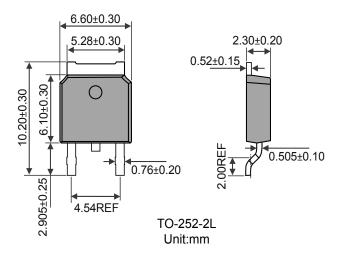


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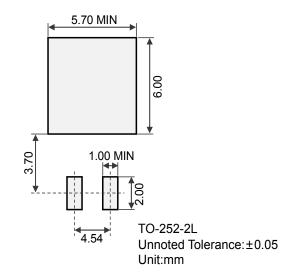


### **Package Outline Dimensions & Suggested Solder Pad Layout**

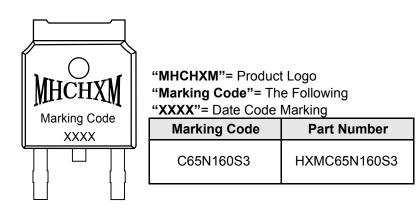
#### **Package Outline Dimensions**



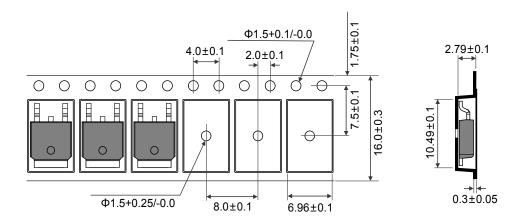
#### **Suggested Solder Pad Layout**



## **Marking Information**



## **Reel Tape Dimensions (Dimensions in mm)**





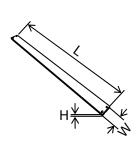
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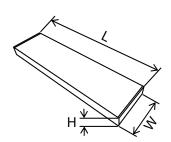
## **Packing Information**

Packaging	Part Number	Quantity(pcs)	Size(mm)
	Tube	80	L540×W20×H5
Tube	Inner Box	4000	L570×W115×H55
	Outer Box	20000	L595×W320×H135
	Reel	3000	Ф330×THK17
Reel	Inner Box	3000	L350×W340×H25
	Outer Box	30000	L355×W300×H360

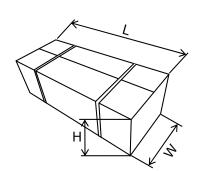
#### Packaging:Tube



Tube

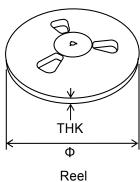


Tube Inner Box

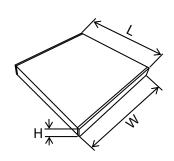


**Tube Outer Box** 

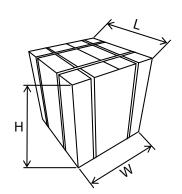
#### Packaging:Reel







Reel Inner Box



Reel Outer Box



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## HXMC65N160S3 N-Channel SiC Power MOSFET MOSFET

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